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EXAMINER SUBCLASS ART UNIT CONT/PRIOR APPLICATION NO. 1745 09/784321

Tākāya Sāto Tatsuo Shimizu

 Multi_{i} -layer electrode structure, and method of manufacturing same

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ISSUING CLASSIFICATION									
ORIG	INAL			CROSS REFERENCE(S)					
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TERMINAL		DRAWINGS		CLAIMS ALLOWED			
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.		
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subsequent to (date) has been disclaimed.	(Assistant Examiner) (Date)				12		
The term of this patent shall			•				
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				Amount Due	Date Paid		
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